

## ABSTRACT OF THE DISCLOSURE

A hard-macro arranged on a semiconductor chip for  
5 constituting a part of a semiconductor integrated circuit  
includes at least one wire passing therethrough. The wire  
is formed in the hard-macro before the hard-macro is  
arranged on the semiconductor chip, and the wire starts a  
first outer edge of the hard-macro and terminates at a  
10 second outer edge of the hard-macro intersecting with the  
first outer edge.